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In an imaging section, an image of an object is converted to signal charge by photodiodes arranged in the form of a matrix, the signal charge is transferred to output circuits by vertical transfer paths, and then the signal charge transferred to the output circuits is converted to signal voltage by the output circuits. signal voltage is output from the output circuits to a signal conversion section as signals. The conversion section performs a processing for the signals in parallel, and outputs the processed signals to a display section. In the display section, signals are converted to voltage by input circuits, and the voltage is respectively applied to drain buses. A vertical scans gate buses, driving circuit and supplies gate driving pulses. Field effect transistors supplied with the gate driving pulses store charge in response to the liquid voltage applied to crystal devices, thus displaying an image.